## KickStart 2 Quick Reference Card

|         | KickStart                       | 2 Switches                                      |
|---------|---------------------------------|---|
| Switch  | Name                            | Ref   |
| SW1-14  | EPROM A1815                     | Table 2.2<br>Page 9                             |
| SW1-5,6 | EPROM SZ1,0                     | Never Change See Table 2.2<br>& Page 10         |
| SW1-7   | Hardware Bypass                 | Default is ON<br>See Page 10                    |
| SW1-8   | Power Detect                    | Default is OFF<br>See Page 10                   |
| SW2-1   | Err Tst#: Hex<br>Display Mode   | Shows POST during boot<br>See Table 3.3 Page 19 |
| SW2-2   | Pause                           | See Table 3.3 Page 10                           |
| SW2-3   | Loop: Loop on<br>Test (LOT)     | See Table 3.3 Page 10                           |
| SW2-4   | LoopErr: Loop on<br>Error (LOE) | If ON will loop on 1st failure                  |
| SW2-5   | StopErr Stop on<br>Error (SOE)  | See Table 3.3<br>Page 19                        |
| SW2-6   | LogCOM1                         | ON-log results to COM1<br>See Table 3.3 Page 19 |
| SW2-7   | LogLpt1                         | ONlog results to Lpt1<br>See Table 3.3 Page 19  |
| SW2-8   | Remote                          | ONrun in remote mode<br>See Table 3.3 Page 19   |
| SW3 18  | Test 70                         | To set test number See<br>Table 3.2 Page 19     |
| SW4     | Reset                           | See Table 3.3 Page 19                           |

|                                      | Meaning and Use of LEDs                 |   |  |  |  |
|--------------------------------------|---|---|--|--|--|
| LED                                  | Color                                   | Meaning Use   |  |  |  |
| Power:<br>+5V<br>-5V<br>+12V<br>-12V | Red<br>Green<br>Yellow<br>Bright<br>red | On if power supply output is within 5%<br>or 2.5% (see description for power level<br>switch SW1-8). Watch LEDs after<br>switching power on, and during heavy<br>disk and memory usage. If any LED<br>goes out or flickers, the supply is bad and<br>should be replaced to avoid data loss. |  |  |  |
| Hex<br>Display                       | 2 Red<br>digits                         | These show the POST or KickStart 2 test<br>currently being or just executed. If no<br>status LEDs (below) are on, it is a POST<br>test. If the Extended Test LED is on,<br>the hex display shows a KickStart 2 test<br>number; otherwise it shows an error<br>number.                       |  |  |  |

|                       | Meaning and Use of LEDs |  |  |  |
|-----------------------|-------------------------|--|--|--|
| LED                   | Color                   | Meaning Use  |  |  |
| Test<br>Run-<br>ning  | Green                   | On if KickStart 2 is running a test and<br>the test is active. Turned on at start of<br>test; turned off when test and all loop<br>passes have finished and test has stopped.<br>If Looping LED is on and Running is off,<br>the test is looping repeatedly on the<br>failing test because StopErr (stop on<br>error) and LoopErr (loop on error) are<br>both on, and the Fail LED will also be<br>on.   |  |  |
| Extend<br>-ed<br>Test | Yellow                  | On if the KickStart 2 hex display is<br>showing a KickStart 2 test number. Off<br>if it is showing a POST code or an<br>KickStart 2 test error code.   |  |  |
| Test<br>Fail          | Red                     | On if a KickStart 2 test just failed; off it<br>just passed. The Fail LED stays on or<br>off throughout the duration of the next<br>test. When it is on, the error code is<br>available to the hex display, provided the<br>Err Tst#switch is on.  |  |  |
| Loop-<br>ing          | Amber                   | KickStart 2 is running a test or batch of<br>tests repeatedly in a loop. If the Fail<br>LED is on and LoopErr switch is on, the<br>test is looping on the failing individual<br>test, but not an entire batch of tests.  |  |  |
| Reset                 | Red                     | Normally Off. On if the reset signal on<br>the system bus is active. This will happen<br>if the processor issues reset, or if you<br>press the front panel reset button (if one<br>is there and wired to the motherboard),<br>or if you press KickStart 2 reset button<br>(and J2 J3is wired to motherboard reset<br>as described in the installation chapter).<br>Power supply can activate reset (via<br>POWERGOOD signal) if it detects its<br>own voltages to be out of tolerance. |  |  |

|            | Power LED Threshold Levels |                   |              |        |        |  |  |
|------------|----------------------------|-------------------|--------------|--------|--------|--|--|
| SW1        | -8 →                       | <u>OFF OFF</u> ON |              |        | ON     |  |  |
| Voltage    | LED                        | <u>-5%</u>        | <u>+5%</u>   | -2.5%  | %2.5%  |  |  |
| +5VDC      | Red                        | <u>+4.75</u>      | +5.25        | +4.875 | +5.125 |  |  |
| -5 VDC     | Green                      | <u>-5.25</u>      | <u>-4.75</u> | -5.125 | -4.875 |  |  |
| +12<br>VDC | Yellow                     | <u>+11.4</u>      | <u>+12.6</u> | +11.7  | +12.3  |  |  |
| -12<br>VDC | Bright<br>Red              | <u>-12.6</u>      | <u>-11.4</u> | -12.3  | -11.7  |  |  |

| Diagnostic Test Numbers and Names |                                     |  |  |  |
|-----------------------------------|-------------------------------------|--|--|--|
| Test # Test Name                  |                                     |  |  |  |
| 00                                | No test                             |  |  |  |
| 01                                | 80x86 8 Central Processor (CPU)     |  |  |  |
| 02                                | 80x87 Math Coprocessor (MPU)        |  |  |  |
| 03                                | CMOS Real Time Clock                |  |  |  |
| 04                                | Speaker                             |  |  |  |
| 06                                | 8259 Interrupt Controller           |  |  |  |
| 07                                | 8253 4IRQ0                          |  |  |  |
| 08                                | 8250 16450IRQ4                      |  |  |  |
| 05                                | 8237 DMA Controller                 |  |  |  |
| 10                                | Data Line Test                      |  |  |  |
| 11                                | Parity Test                         |  |  |  |
| 12                                | March Test                          |  |  |  |
| 13-17                             | Galrow Test 1 through Galrow Test 5 |  |  |  |
| 18                                | Refresh Toggle                      |  |  |  |
| 19                                | Refresh Bandwidth                   |  |  |  |
| 1A                                | Refresh Rate                        |  |  |  |
| 1 <b>B</b>                        | Extended Memory Test                |  |  |  |
| 1F                                | CRT RAM                             |  |  |  |
| 24                                | Video Mode                          |  |  |  |
| 20                                | 84-Key                              |  |  |  |
| 21                                | 101-Key                             |  |  |  |
| 30                                | Format Random                       |  |  |  |
| 31                                | Write Random                        |  |  |  |
| 32                                | Read Random                         |  |  |  |
| 33                                | Seek Random                         |  |  |  |
| 34                                | Format Entire                       |  |  |  |
| 35                                | Write Read Random                   |  |  |  |
| 39                                | Format Random                       |  |  |  |
| 3A                                | Write Random                        |  |  |  |
| 3B                                | Read Random                         |  |  |  |
| 3C                                | Write Read CompareEntire            |  |  |  |
| 3D                                | Write Read CompareTrack 0           |  |  |  |
| 3E                                | Format Entire (Interleave = 2)      |  |  |  |
| 3F                                | Park Heads                          |  |  |  |
| 50                                | Data Line                           |  |  |  |
| 51                                | Asynchronous I O                    |  |  |  |
| 58                                | Data Line                           |  |  |  |
| 59                                | Command Line                        |  |  |  |
| 5A                                | DATA to STATUS                      |  |  |  |
| 5B                                | DATA to COMMAND                     |  |  |  |
| 5C                                | Toggle Line                         |  |  |  |
| 90                                | Western Digital WD 8003 family      |  |  |  |

|      |  | c Test Numbers and Names  |  |  |  |  |
|------|--|---|--|--|--|--|
| Test |  | Test Name   |  |  |  |  |
| 91   |  | -COM EtherCard  |  |  |  |  |
| 92   | N                                      | lovell NE 1000 family   |  |  |  |  |
|      |  |   |  |  |  |  |
|      |  | Cest Error Codes and Meanings   |  |  |  |  |
| Code |  | Comment or Corrective Action  |  |  |  |  |
|      | F                                      | KickStart 2 Self-Test   |  |  |  |  |
| 01   | KickStart 2<br>initialization<br>error | KickStart 2 cannot execute its own<br>hardware test, indicating a memory<br>or I Ospace conflict in the system.<br>Recheck DIP switches SW1 and SW2,<br>and orremove other expansion cards<br>from the system.                                |  |  |  |  |
| 02   | KickStart 2<br>hardware test<br>error  | Some KickStart 2 component is<br>broken. Possible (but unlikely) I O<br>space conflict. If this error occurs in<br>a known-good computer, contact<br>KickStart 2 technical support.   |  |  |  |  |
|      |  | CPU Test  |  |  |  |  |
| 04   | CPU register<br>test failed            | The 8088 or 80x86 CPU failed its<br>register test. This is extremely<br>unlikely, since the CPU must be<br>running to get this far. It is more<br>likely that the CPU is running at a<br>clock speed faster than it is rated.<br>Replace CPU. |  |  |  |  |
|      | M                                      | ath Coprocessor Test  |  |  |  |  |
| 08   | Math<br>coprocessor<br>test failed     | The 8087, 80287, or 80387 math chip<br>failed to function properly. This may<br>be due to running at a speed greater<br>than rated, or a bad bus<br>interface resetcircuit. Replace math<br>chip. If problems persist, check its<br>socket.   |  |  |  |  |
|      |  | Refresh Test  |  |  |  |  |
|      | No RAM<br>refresh toggle               | o RAM   |  |  |  |  |
| 0A   | RAM refresh<br>rate out of<br>limits   | AM refresh<br>te out of Many clones intentionally use a   |  |  |  |  |
| 0B   | Real Time<br>Clock error               | The RTC is not properly timing an event. Replace the RTC chip.  |  |  |  |  |

|      | Diagnostic T   | est Error Codes and Meanings  |  |  |  |  |
|------|--|---|--|--|--|--|
| Code | Meaning  | Comment or Corrective Action  |  |  |  |  |
|      | RAM Test   |   |  |  |  |  |
| 0C   | RAM data<br>compare error                              | The test could not verify data at a<br>given memory address. This is most<br>likely due to the absence of a<br>memory chip, or chips. Re-check<br>DIP SIMM installation for<br>arrangement, jumpers, orientation.<br>Look at the error message for the<br>failing address data, and replace chip<br>if necessary.   |  |  |  |  |
| 0D   | RAM even<br>parity error                               | A parity error has occurred at the<br>address in the error message,<br>although the data is correct. This is<br>due to a bad RAM chip, or running   |  |  |  |  |
| 0E   | RAM odd<br>parity error                                | the system at too high a clock speed<br>for the parity circuit. Replace the<br>offending DRAM chip if possible, or<br>run the system at a slower speed.   |  |  |  |  |
| OF   | Address<br>conflict error                              | Data was written to one address that<br>was intended for another address.<br>The message shows both addresses.<br>Suspect a bad DRAM external<br>RAS CASline or internal address<br>line, bad socket, cold solder joint,<br>running the DRAM too fast, or a bad<br>address multiplexer. Swap RAM<br>chips; if symptoms change, suspect<br>bad DRAM chip. If bad addresses<br>are a power of 2 apart (e.g., 32K,<br>64K, 128K) look for floating address<br>line by comparing levels. If bad<br>addresses are a sum of two multiples<br>of 2 (e.g., 24K, 48K, 96K), look for<br>two shorted lines. Check at the<br>DRAM chip itself, and the address<br>multiplexer. |  |  |  |  |
|      | 8253 (XT   | ) or 8254 (AT) Timer Test   |  |  |  |  |
| 10   | 8253 4 timer<br>controller<br>failed                   | The test cannot access the system timer at 40h. Replace it.   |  |  |  |  |
|      |  | Speaker Test  |  |  |  |  |
|      | Speaker<br>doesn't beep<br>(no error<br>display given) | The speaker is driven from timer<br>channel 2, but does not respond.<br>Check the timer. Check that the<br>speaker is connected. Check the<br>speaker gate control works by<br>examining circuit in 'Loop on Error'<br>mode.  |  |  |  |  |

|                  | Diagnosoc To  | est Error Codes and Meanings  |  |  |  |
|------------------|---|---|--|--|--|
| Code             | Meaning   | Comment or Corrective Action  |  |  |  |
| 2F               | Can't disable<br>IRQ with I O<br>Channel<br>printer serial<br>port data | The test cannot remove an interrupt<br>from the I Ochannel. Either a<br>conflict exists on the I OChannel or<br>the input to the 8259 is faulty.<br>Remove all cards from the system. If<br>problem remains, look for<br>Serial Parallelon motherboard, then<br>replace 8259. |  |  |  |
|                  |   | Keyboard Test   |  |  |  |
| 34               | Keyboard<br>Error   | A key error occurred. Rerun test,<br>then replace Keyboard.   |  |  |  |
|                  |   | Printer Port Test   |  |  |  |
| x8               | Command<br>port error   | These errors show expected versus received data. A conflict exists with   |  |  |  |
| x9               | Status port<br>error  | another adapter or two printer<br>ports interrupts, or running the port<br>at too high a bus speed. Replace   |  |  |  |
| хA               | Control port<br>error   | at too high a bus speed. Replace<br>port circuitry or reconfigure the port  |  |  |  |
| хB               | Data port<br>error  | Note: These tests require a loopbac<br>plug on the port tested (any of<br>three). Errors 3x are for LPT1 (por<br>378, IRQ7); 4x are for LPT2 (port<br>278, IRQ5); 5x are for LPT3 (port<br>3BC, INT7).  |  |  |  |
| xC               | Driver toggle<br>error (can't<br>turn on off<br>port driver)            |   |  |  |  |
| Serial Port Test |   |   |  |  |  |
| x0               | Framing error   | A conflict exists with another adapter or two serial ports interrupts, or   |  |  |  |
| x1               | Overrun error   | running the port at too high a bus<br>speed. Reconfigure the port. If still<br>failing, replace 8250 16450serial chip,  |  |  |  |
| x2               | Parity error  | or RS-232C RS-422 driver receiver.  |  |  |  |
| x3               | Data error<br>Transmit  | Note: These tests require a loopback plug on the port tested. Errors $4x$   |  |  |  |
| x4               | timeout error   | are COM1 (3F8, IRQ4), 5x for<br>COM2 (2F8, IRQ3), 6x for COM3   |  |  |  |
| x5               | Receive<br>timeout error  | (3E8, IRQ4), 7x for COM4 (2E8,<br>IRQ4).  |  |  |  |
|                  |   | Video Test  |  |  |  |
| x8               | 6845 registers<br>failed  |   |  |  |  |
| x9               | Other latches, registers failed   | These tests check video adapter base<br>registers. Errors 6x are for mono<br>(I Oaddress 3B0), and 7x are for   |  |  |  |
| хA               | Video memory<br>error   | color (3D0). EGA is tested at 3C0h,<br>in addition to its mono or color   |  |  |  |
| хB               | Extended<br>EGA VGA<br>registers failed                                 | mode.   |  |  |  |

|                           | Diagnostic Te  | est Error Codes and Meanings  |  |  |
|---------------------------|--|---|--|--|
| Code                      | Meaning  | Comment or Corrective Action  |  |  |
| 8237A DMA Controller Test |  |   |  |  |
| 18                        | Controller<br>failed   | The test cannot read write the DMA controller. Replace it.  |  |  |
| 19                        | Page register<br>failed  | The DMA Page register, which<br>controls Address lines 16-19 during<br>DMA, does not work. Replace it.  |  |  |
| 1A                        |  | The test cannot move a block of data<br>from one area of memory to another<br>via DMA. Suspect faulty bus                                       |  |  |
| 1B                        | 8-bit RAM-to-<br>I Otransfer   | transceiver, or strange motherboard<br>design (some properly functioning<br>motherboards cannot pass this test).                                |  |  |
| 1                         | -  | nterrupt Controller (8259A) Tests   |  |  |
| 20                        | Interrupt mask<br>register<br>incorrect  | The test cannot read write the mask register. Replace the 8259A.  |  |  |
| 21                        | Unexpected<br>ISR bit set  | There is an unexpected Interrupt In<br>Service bit set. Insert JumpStart<br>BIOS, and retry test. If the problem<br>remains, replace the 8259A. |  |  |
| 22                        | Can't clearThe test cannot clear an In ServiceISR bitRequest. Replace the 8259A. |   |  |  |
| 23                        | Unknown fatal<br>failure   | There is an unexpected failure.<br>Rerun test with JumpStart BIOS<br>installed. If still fails, replace the<br>8259A.                           |  |  |
| 24                        | No interrupt<br>occurred   | No interrupt occurred from the timer. Check timer, and then replace the 8259A.  |  |  |
| 28                        | Can't disable<br>parity circuit  | The test cannot disable the parity circuit for testing. Check Parity circuit.   |  |  |
| 29                        | NMI not<br>received  | The test did not receive a requested NMI. Check NMI circuit.  |  |  |
| 2A                        | Unexpected<br>NMI  | The test received an unexpected NMI. Check NMI circuit.   |  |  |
| 2B                        | Interrupt<br>pending bit<br>not cleared  | The Interrupt pending bit was not<br>cleared as the interrupt was removed.<br>Replace 8259A.  |  |  |
| 2C                        | Wrong<br>interrupt<br>source   | Replace 8259A.  |  |  |
| 2D                        | Wrong priority   | Replace 8259A.  |  |  |
| 2E                        | Interrupt<br>pending bit<br>not set after<br>reset                               | Replace 8259A.  |  |  |

| <u> </u>                   | Diagnostic Test Error Codes and Meanings |        |   |                              |   |  |  |
|----------------------------|--|--------|---|------------------------------|---|--|--|
| Code                       |  |        |   |                              | nt or Corrective Action                             |  |  |
| Floppy Drive Tests         |  |        |   |                              |   |  |  |
| 80                         | Read failu                               |        |   |                              |   |  |  |
| 81                         | Write fail                               | ure    | Check cables between drive and  |                              |   |  |  |
| 82                         | Format fa                                | ilure  | controller circuit, and power supply<br>lure cable going to drive. Possibly drive |                              |   |  |  |
| 83                         | Seek failu                               | re     | ID  | jumpers                      | or cable type are                                   |  |  |
| 86                         | RAM requerror                            | uest   |   |                              |   |  |  |
| 8C                         | Interrupt<br>timeout                     |        | 1   | ntroller.                    | oloni wili interrupt                                |  |  |
|                            |  |        | Ha  | rd Disk '                    | <b>Fests</b>  |  |  |
| x1                         | Read failu                               | ire    | Ch  | eck cable                    | s between drive and                                 |  |  |
| x2                         | Write fail                               | ure    |   |                              | ircuit, and power supply                            |  |  |
| x3                         | Format fa                                | ilure  | ID  | jumpers                      | to drive. Possibly drive<br>or cable type are       |  |  |
| x4                         | Compare<br>failure                       |        | car   | d or inte                    | Replace the disk controller<br>rface chip. Possible |  |  |
| xo i                       | Drive(s) n<br>present                    | ot     | ſ   |                              | th interrupt controller.                            |  |  |
| x7                         | Unsuccess<br>info reque                  |        | Note: Errors Cx are for drive 0 (C:),<br>and Dx are for drive 1 (D:)              |                              |   |  |  |
|                            | CMOS                                     | S Rea  | 1 Ti  | me Cloci                     | k Test (AT only)                                    |  |  |
|                            | System not                               |        |   |                              |   |  |  |
|                            | initialized<br>checksum                  |        | err Check or replace the CMOS   |                              |   |  |  |
|                            | RTC RAN                                  | _      |   |                              |   |  |  |
| F2                         | write read                               |        | 1.  |                              | contery of emp.                                     |  |  |
|                            |  |        |   |                              |   |  |  |
|                            | SW1 - 1                                  | EPRC   | M   | Address                      | and Window Size                                     |  |  |
|                            | SW1 Swi                                  | tch Se | ettir   | ıg                           | Meaning   |  |  |
| SW1-                       | 1 SW1-2                                  | SW     | 1-3   | SW1-4                        | EPROM Start Address                                 |  |  |
| Off                        | Off                                      | Off    |   | Off                          | F8000   |  |  |
| Off                        | Off                                      | Off    |   | On                           | F0000   |  |  |
| Off                        | Off                                      | On     |   | Off                          | E8000   |  |  |
| Off                        | Off                                      | On     |   | On                           | E0000   |  |  |
| Off                        | On                                       | Off    |   | Off                          | D8000   |  |  |
| Off                        | On                                       | Off    |   | <u>On</u>                    | D0000 (default)                                     |  |  |
| Off                        | On                                       | On     |   | Off                          | C8000   |  |  |
| Off                        | On                                       | On     |   | On                           | C0000   |  |  |
| SW1-                       |  |        |   |                              | Window Size   |  |  |
| Off                        | Off                                      |        |   |                              | 256K  |  |  |
| Off                        | On                                       |        |   |                              | 128K  |  |  |
| On                         | On                                       |        |   |                              |   |  |  |
| Off<br>SW1-S<br>Off<br>Off | On<br>5 <b>SW1-6</b><br>Off<br>On        |        |   | C0000<br>Window Size<br>256K |   |  |  |

| Symptom   | Diagnostic Testing Strategy Problem Action   |
|---|--|
| ~ <u>j</u>  |  |
| POST code<br>shows or BIOS<br>beeps more<br>than once, but<br>no display                      | Troubleshoot circuit based on BIOS<br>beeps POSTcode; refer to BIOS and<br>POST codes topics in this chapter, and<br>check with BIOS manufacturer for<br>meaning of beeps. May need to install or<br>correct the setup and installation of video<br>adapters to correct the beep problem.<br>Check the monitor function and<br>adjustment. Replace failing component<br>as indicated by POST code; use<br>JumpStart BIOS if you are unsure of<br>POST codes. |
| BIOS beeps<br>more than once,<br>also get display   | Troubleshoot circuit based on nature of<br>display. If disk problem, ensure disk<br>drives and controller are installed and<br>connected properly, disk is low-level<br>formatted properly, boot disk is high-<br>level formatted properly for operating<br>system being used, and current correct<br>operating system is on boot disk. If you<br>are unsure, install JumpStart BIOS and<br>run tests from KickStart 2.                                      |
| BIOS beeps<br>once, but no<br>display   | Monitor is not connected, is off, or needs<br>to be adjusted brighter. Connect it to<br>the video adapter and switch it on, ensure<br>brightness and contrast adjustments are<br>correct.  |
| BIOS beeps<br>once and<br>displays, but<br>something<br>doesn't seem<br>right                 | Run comprehensive system, hard drive,<br>and floppy drive diagnostic and<br>calibration software as necessary.<br>Contact Landmark for information on<br>products available to help with problems.   |
| Power LEDs<br>are on, but<br>KickStart 2<br>shows error via<br>screen, LED, or<br>POST codes. | Look up POST code in the POST code<br>table for your BIOS. Contact BIOS<br>manufacturer for more information as<br>needed. Use JumpStart BIOS that comes<br>with KickStart 2 for accurate POST code<br>definition. Replace indicated bad<br>component.   |
| Display ok, no<br>POST code,<br>system boots<br>but doesn't run<br>right                      | Run KickStart 2 diagnostics from<br>KickStart 2 menu. Refer to KickStart 2<br>diagnostic test error codes and<br>troubleshooting tips in this chapter, and<br>replace the faulty component.  |
| No POST code,<br>system boots,<br>KS2 menu does<br>not start                                  | 82C601 Conflict. Computer system not<br>using port 80 for POST codes.  |

|   | Diagnostic Testing Strategy   |
|---|---|
| Symptom   | Problem Action  |
| Nothing<br>happens at all   | Power is off. Plug into wall and switch<br>on power to all system components and<br>look for power light on front of<br>computer, listen for fan running inside<br>computer. Smell and look for signs of<br>burning. If the fan is off, replace power<br>supply, otherwise continue.<br>Open the computer and install<br>KickStart 2 in accordance with   |
| Power and fan<br>are on but<br>nothing<br>happens at all  | instructions in the Installation chapter.<br>Power supply disconnected from<br>motherboard. Plug power supply<br>connectors in. Be sure the correct plugs<br>are on the correct power jacks; refer to<br>table in Technical Information chapter<br>for reference. If plugged in, check the<br>power LEDs on KickStart 2. Make sure<br>the KickStart 2 Reset LED is off. Listen<br>for the fan changing pitch or power<br>LEDs flashing on and off (anything but<br>steady on is bad). |
| Fan changing<br>pitch   | Incoming power is varying, typical during<br>peak load times. Consider buying an<br>uninterruptable power supply that<br>prevents brown-out or black-out. A<br>cheap supply in your computer can<br>damage components or allow data faults<br>to occur without your knowing it. Some<br>high-quality supplies have a fan that<br>changes speed as temperature increases<br>or decreases, and fan-pitch changes are<br>no concern.   |
| KickStart 2<br>power LED(s)<br>off or flashing  | Power supply is bad or power is bad to<br>the expansion slot containing KickStart 2.<br>Try KickStart 2 in other slots. If LEDs<br>still off flashing, replace the supply.  |
| Power LEDs<br>on, but: Reset<br>LED on; or<br>KS2 won't run<br>remote tests; or<br>no BIOS beeps,<br>no POST codes,<br>no display | Major motherboard circuits dead. Look<br>at failing POST code on KickStart 2, and<br>troubleshoot corresponding circuit; see<br>POST code tables in this manual, or<br>check with BIOS manufacturer. To be<br>sure, install JumpStart BIOS on<br>motherboard first. You may need to<br>replace data or address bus chip, CPU, or<br>clock chip, in that order. Refer to<br>motherboard chipset type in this chapter.  |