

PCI IRQ Activated By

We suggest that you set this to its default configuration unless you are a qualified technician.

The options are: Level (Default), Edge

PCI IDE IRQ Map To

Set "PCI-AUTO" to allow the system BIOS to automatically detect the add-on PCI IDE card interrupt used by the PCI hard disk drive.

The options are: PCI-AUTO (Default), PCI-SL0T1, PCI-SL0T2, PCI-SL0T3, PCI-SL0T4, ISA

CPU to PCI Write Buffer

When enabled, allows data and address access to the internal buffer of VT82C505 so that the processor can be released from the wait state.

The options are: Enabled (Default), Disabled

PCI Master Write Buffer

When enabled, allows the PCI write operation by informing the processor of pending data from the PCI device. Processor is released from waiting state by a signal from the master card.

The options are: Enabled (Default), Disabled

PCI Master Prefetch

When enabled, allows the data and address to be saved in the internal buffer of VT82C505 to reduce master drive access time.

The options are: Enabled (Default), Disabled

PCI Master 1WS Write

When enabled, allows one more wait state cycle delays when the PCI master drive writes data to DRAM.

The options are: Enabled, Disabled (Default)

Interrupt Assignments of PCI Slots

SLOT	INT OF SLOT	INT OF VT82C505
PCI Slot 1	A	A
	B	B
	C	C
	D	D
PCI Slot 2	A	B
	B	C
	C	D
	D	A
PCI Slot 3	A	C
	B	D
	C	A
	D	B
PCI Slot 4	A	D
	B	A
	C	B
	D	C

Load BIOS Defaults

BIOS defaults contain the most appropriate values of the system parameters that allow minimum system performance. The OEM manufacturer may change the defaults through MODBIN before the binary image burns into the ROM.

Load Setup Defaults

Selecting this field loads the factory defaults for BIOS and Chipset Features which the system automatically detects.