| PIN NUMBER | SIGNAL | SIGNAL DIRECTIO | N FUNCTION |
|------------|-------------------|---------------------------|--|
| 1 | DATA STROBE -N | <u>-</u> | Samples the input data when |
| 2 | DATA BIT 1 — | | changing from low to high. |
| 3 | DATA BIT 2 | | |
| 4 | DATA BIT 3 | | |
| 5 | DATA BIT 4 | TO PRINTER | Indicates the input data. High level signal shows a 1, and low |
| 6 | DATA BIT 5 | | level signal shows a 0. |
| 7 | DATA BIT 6 | | |
| 8 | DATA BIT 7 | | |
| 9 | DATA BIT 8 | | |
| 10 | ACKNOWLEDGE -N | FROM PRINTER | At a low level, indicates that a character input is complete |
| 11 | BUSY | FROM PRINTER | or that a function operation is finished. At a high level, data reception is impossible. At a low level, data reception is possible. |
| 12 | PAPER END | FROM PRINTER | At a high level, indicates paper end. |
| 13 | BUSY -N SELECT | FROM PRINTER FROM PRINTER | Tandy mode: At a low level, data reception is impossible. At a high level, data reception is possible. IBM mode: At a high level, indicates that the printer is in select mode (data reception possible). |
| 14 | NC | | Tandy mode IBM mode |
| 16 | AUTO LF -N OV | | Signal ground |
| 17 | CHASSIS GROUND | FRAME GROUND | |
| 18 | +5∨ | FROM PRINTER | Tandy Mode: +5V power supply (Max. 50 mA) IBM mode |
| | NC | | |
| 19-30 | OV | | Twisted pair return (for pins #1 through 11). |
| 31 | NC | | Tandy mode |

| | INPUT PRIME-N | TO PRINTER | IBM Mode: When this goes low, the printer control section is initialized. The low level period should be more than 0.5 ms. |
|-------|-------------------|--------------|--|
| 32 | FAULT-N | FROM PRINTER | This signal goes from high to low lever when paper end. |
| 33 | INIT OV | TO PRINTER | Tandy Mode IBM Mode |
| 15,34 | NC | | Unused pine (No connection) |
| 35 | NC Pulled High | | Tandy Mode IBM Mode |
| 36 | NC SLCTIN-N | | Tandy Mode IBM Mode |

 $^{^{\}star}$ IBM interface mode is achieved by moving jumpers located inside the printer. See your local Radio Shack Service Center for this change.

low level $0.0V \sim +0.8V$ high level $+2.4V \sim +5.0V$

(jlc-07/26/93)

^{*} Parallel Interface Level