INTERNAL MODEM REGISTERS

The 1037B has 19 registers. These registers are memory locations inside the modem. These registers control some aspects of it's operation. The default values will work for most applications.

REGISTER	SUMMARY

REGISTER	RANGE	UNIT	DEFAULT	DESCRIPTION
SO	0-255	rings	0	Ring to answer on.
S1	0-255	rings	0	Number of rings passed.
S2	0-127	ASCII	43	Escape code character.
S3	0-127	ASCII	13	Command terminator.
S 4	0-127	ASCII	10	Line feed character.
S5	0-32,127	ASCII	8	Back space character.
S6	2-255	seconds	2	Wait time for dial tone.
S7	0-255	seconds	30	Wait time for carrier.
S8	0-255	seconds	2	Pause time for comma.
S9	1-255	1/10 sec.	6	Carrier detect response time.
S10	0-255	1/10 sec.	14	Carrier loss hang up delay.
S11	0-255	1/100 sec	95	Touch-tone timing.
S12	0-255	1/50 sec.	50	Escape code timing.
S13		Not used.		
S14		Bit Mapped.		Option Register.
S15		Not used.		
S16		Bit Mapped.		Option Register.
S17		Not used.		
S18	0-255	seconds	0	Test duration.
S19		Not used.		
S20		Not used.		
S21		Bit Mapped.		Option register.
S22		Bit Mapped.		Option register.
S23		Bit Mapped.		Option register.
S24		Not used.		
S25		Not used.		
S26		Not used.		
S27		Bit Mapped.		Option register.

VIEWING REGISTERS To view the contents of a register, in the command mode type: AT Sr?<ENTER> Where r is the register number. Your modem returns: Where nn is the current setting of the nn register. OK You can view the contents of several registers with one command: AT Sr? Sr? Sr? Your modem returns: First register. nn nn Second register. nn Third register. OK

SETTING REGISTERS To change the contents of a register, in the command mode type: AT Sr=n<ENTER> Where r is the register number and n is the new value. Your modem returns: OK You can set the contents of several registers with one command: AT Sr=n SR=n Sr=n <ENTER> Your modem returns: OK Register S0 - Auto Answer Control This register determines how many times your phone rings before your modem answers. If you set this register to 0, your modem does not answer the telephone. The default value for this register is 0. Register S1 - Ring Counter This register contains the count of the current number of rings. If your telephone does not ring again within 8 seconds, this register resets to 0. You should never need to change the value in this register. Register S2 - Escape Code ASCII Value This register contains the ASCII value of the escape code. You must set this value to a legitimate ASCII character within the range 0-127. Setting this code to greater than 127 disables the escape code. See "Switching From the Communications to the Command Mode" in "Dialing and Answering with the AT Command Set", earlier in this manual. The default value for this register is 43 (ASCII value of "+"). Register S3 - Command Terminator This register contains the ASCII value of the character you use to end and execute a command. You should never need to change this value. The default for this register is 13 (ASCII value of ENTER). Register S4 - Line Feed ASCII Value This register sets the character your modem sends after any result code. You should never need to change this value. If you do not want the modem to send a line feed after the result code, set this register to 0. The default to this register is 10 (ASCII value for line feed). Register S5 - Backspace ASCII Value This register sets the character your modem uses as a backspace character for editing. You do not normally need to change this register. The default value is 8 (ASCII backspace). Register S6 - Wait for Dial Tone Duration This register sets how many seconds your modem waits to begin dialing after it goes off hook. If your modem begins dialing before the dial tone starts, your call might not go through. You can set this register to a higher value to provide a longer delay. The default value is 2 (setting to 0 or 1 also gives a 2 second delay). Register S7 - Wait for Carrier Duration This register sets how long your modem waits after dialing to detect a carrier from an answering modem. If this time expires without a carrier detect, your modem returns the NO CARRIER result code. The default value

is 30.

Register S8 - Dialing Pause (,) Duration This register sets how many seconds your modem pauses when it encounters a comma (,) in the dialing sequence. If you dial from a system that requires a second dial tone (like a PABX system), you might need to increase the duration of this pause if your modem is not waiting long enough for an outside line. The default value is 2.

Register S9 - Carrier Detect Response Time Sets the length of time for the modem to acknowledge a valid carrier. Default is 6/10 seconds.

Register S10 - Carrier Loss To Disconnect Duration This register sets how many tenths of a second (1/10 seconds) your modem waits to disconnect from the phone line after the other modem's carrier stops. In some areas with poor quality phone service, the carrier can occasionally drop out and cause your modem to disconnect during a call. If you set this register to 255, your modem considers the carrier is always present. The default value is 14 (1.4 seconds).

Register S11 - Touch Tone Dialing Speed This register controls the dialing rate during tone dialing (no affect on pulse dialing). The default value of 95 sets a rate of about seven digits per second.

Register S12 - Escape Code Guard Time This register sets the length of time (in 1/50 second increments) you must pause before and after the escape code for your modem to recognize the escape code. See "Switching From the Communications to the Command Mode" in "Dialing and Answering with the AT Command Set", earlier in this manual. The default value is 50 (1 second).

Register S13 - Not Used

Register S14 - Bit Mapped Options This register is a bit-mapped register that shows the status of some of the operation options. Normally, you do not write to this register. You use specific commands to set these options.

Bit	Value	Description
0	Not used.	
1	0	Local echo disabled.
	1	Local echo enabled.
2	0	Result codes enabled.
	1	Result codes disabled.
3	0	Result codes as numbers.
	1	Result codes as words.
4	Not used.	
5	0	Tone dial.
	1	Pulse dial.
6	Not used.	
7	0	Answer mode.
	1	Originate mode.

Register S15 - Not Used

Register S16 - Test Mode Option Register

This register is a bit-mapped register that shows the status of the test functions. Normally you do not write to this register. You use the &T command to change these options. See "Modems Test Commands" for more information. Bit Value Description

informat	lon.	
Bit	Value	Description
0	0	Analog loopback disabled.
	1	Analog loopback enabled (&T1).
1	Not used.	1
2		Digital loophack disabled
2	0	Digital loopback disabled.
	1	Digital loopback enabled (&T3).
3	0	Loopback not in process.
	1	Responding to remote digital loopback.
4	0	Remote digital loopback disabled.
	1	Remote digital loopback enabled (&T6).
5	0	Remote digital loopback with self test disabled.
J		
	1	Remote digital loopback with self test enabled (&T7).
6	0	Analog loopback with self test disabled.
	1	Analog loopback with self test enabled (&T8).
7	Not used.	
Register	S17 - Not Use	d
2		
Register	S18 - Test Ti	mer
-		the duration in seconds for the test. The default
		s the modem to perform any test until you cancel it
with the	&T0 command.	See "Modem Test Commands" for more information.
Register	S19 - Not Use	d
2		
Pagistar	S20 - Not Use	d
Regiscer	SZU NOC USE	
	S21 - Bit Map	
		ped Options -mapped register that shows the status of the &D,&C,
This reg	ister is a bit	-mapped register that shows the status of the &D,&C,
This reg and Y co	ister is a bit mmands. You do	-mapped register that shows the status of the &D,&C, not normally write to this register. Use the
This reg and Y co individu	ister is a bit mmands. You do al commands to	-mapped register that shows the status of the &D,&C, not normally write to this register. Use the affect these options.
This reg and Y co individu Bit	ister is a bit mmands. You do al commands to Value	-mapped register that shows the status of the &D,&C, not normally write to this register. Use the
This reg and Y co individu Bit 0	ister is a bit mmands. You do al commands to Value Not used.	-mapped register that shows the status of the &D,&C, not normally write to this register. Use the affect these options.
This reg and Y co individu Bit	ister is a bit mmands. You do al commands to Value Not used. Not used.	-mapped register that shows the status of the &D,&C, not normally write to this register. Use the affect these options.
This reg and Y co individu Bit 0	ister is a bit mmands. You do al commands to Value Not used.	-mapped register that shows the status of the &D,&C, not normally write to this register. Use the affect these options.
This reg and Y con individu Bit 0 1 2	ister is a bit mmands. You do al commands to Value Not used. Not used. Not used.	-mapped register that shows the status of the &D,&C, not normally write to this register. Use the affect these options. Description
This reg and Y con individu Bit 0 1	ister is a bit mmands. You do al commands to Value Not used. Not used. Not used. 00	<pre>-mapped register that shows the status of the &amp;D,&amp;C, not normally write to this register. Use the affect these options. Description &amp;D0 (Forced DTR)</pre>
This reg and Y con individu Bit 0 1 2	ister is a bit mmands. You do al commands to Value Not used. Not used. Not used. 00 10	<pre>-mapped register that shows the status of the &amp;D,&amp;C, not normally write to this register. Use the affect these options. Description &amp;D0(Forced DTR) &amp;D1(Async command state)</pre>
This reg and Y con individu Bit 0 1 2	ister is a bit mmands. You do al commands to Value Not used. Not used. Not used. 00 10 01	<pre>-mapped register that shows the status of the &amp;D,&amp;C, not normally write to this register. Use the affect these options. Description &amp;D0(Forced DTR) &amp;D1(Async command state) &amp;D2(On hook async command state, disables auto answer)</pre>
This reg and Y con individu Bit 0 1 2 3,4	ister is a bit mmands. You do al commands to Value Not used. Not used. Not used. 00 10 01 11	<pre>-mapped register that shows the status of the &amp;D,&amp;C, not normally write to this register. Use the affect these options. Description &amp;D0(Forced DTR) &amp;D1(Async command state) &amp;D2(On hook async command state, disables auto answer) &amp;D3(Assumes initialization state)</pre>
This reg and Y con individu Bit 0 1 2	ister is a bit mmands. You do al commands to Value Not used. Not used. Not used. 00 10 01 11 0	<pre>-mapped register that shows the status of the &amp;D,&amp;C, not normally write to this register. Use the affect these options. Description &amp;D0(Forced DTR) &amp;D1(Async command state) &amp;D2(On hook async command state, disables auto answer) &amp;D3(Assumes initialization state) &amp;C0(Carrier detect forced)</pre>
This reg and Y con individu Bit 0 1 2 3,4	ister is a bit mmands. You do al commands to Value Not used. Not used. Not used. 00 10 01 11 0	<pre>-mapped register that shows the status of the &amp;D,&amp;C, not normally write to this register. Use the affect these options. Description &amp;D0(Forced DTR) &amp;D1(Async command state) &amp;D2(On hook async command state, disables auto answer) &amp;D3(Assumes initialization state)</pre>
This reg and Y con individu Bit 0 1 2 3,4 5	ister is a bit mmands. You do al commands to Value Not used. Not used. Not used. 00 10 01 11 0	<pre>-mapped register that shows the status of the &amp;D,&amp;C, not normally write to this register. Use the affect these options. Description &amp;D0(Forced DTR) &amp;D1(Async command state) &amp;D2(On hook async command state, disables auto answer) &amp;D3(Assumes initialization state) &amp;C0(Carrier detect forced)</pre>
This reg and Y con individu Bit 0 1 2 3,4	ister is a bit mmands. You do al commands to Value Not used. Not used. 00 10 01 11 0 1 11 0 1 Not used.	<pre>-mapped register that shows the status of the &amp;D,&amp;C, not normally write to this register. Use the affect these options. Description &amp;D0 (Forced DTR) &amp;D1 (Async command state) &amp;D2 (On hook async command state, disables auto answer) &amp;D3 (Assumes initialization state) &amp;CO (Carrier detect forced) &amp;C1 (Carrier detect true)</pre>
This reg and Y con individu Bit 0 1 2 3,4 5 5	ister is a bit mmands. You do al commands to Value Not used. Not used. 00 10 01 11 0 1 1 Not used. 0	<pre>-mapped register that shows the status of the &amp;D,&amp;C, not normally write to this register. Use the affect these options. Description &amp;D0 (Forced DTR) &amp;D1 (Async command state) &amp;D2 (On hook async command state, disables auto answer) &amp;D3 (Assumes initialization state) &amp;C0 (Carrier detect forced) &amp;C1 (Carrier detect true) Y0 (Disable long space disconnect)</pre>
This reg and Y con individu Bit 0 1 2 3,4 5 5	ister is a bit mmands. You do al commands to Value Not used. Not used. 00 10 01 11 0 1 1 Not used. 0	<pre>-mapped register that shows the status of the &amp;D,&amp;C, not normally write to this register. Use the affect these options. Description &amp;D0 (Forced DTR) &amp;D1 (Async command state) &amp;D2 (On hook async command state, disables auto answer) &amp;D3 (Assumes initialization state) &amp;CO (Carrier detect forced) &amp;C1 (Carrier detect true)</pre>
This reg and Y con individu Bit 0 1 2 3,4 5 6 7	ister is a bit mmands. You do al commands to Value Not used. Not used. 00 10 01 11 0 1 Not used. 0 1 Not used. 0 1	<pre>-mapped register that shows the status of the &amp;D,&amp;C, not normally write to this register. Use the affect these options. Description &amp;D0 (Forced DTR) &amp;D1 (Async command state) &amp;D2 (On hook async command state, disables auto answer) &amp;D3 (Assumes initialization state) &amp;C0 (Carrier detect forced) &amp;C1 (Carrier detect true) Y0 (Disable long space disconnect) Y1 (Enable log space disconnect)</pre>
This reg and Y con individu Bit 0 1 2 3,4 5 6 7 Register	ister is a bit mmands. You do al commands to Value Not used. Not used. 00 10 01 11 0 1 Not used. 0 1 S22 - Bit Map	<pre>-mapped register that shows the status of the &amp;D,&amp;C, not normally write to this register. Use the affect these options. Description &amp;D0 (Forced DTR) &amp;D1 (Async command state) &amp;D2 (On hook async command state, disables auto answer) &amp;D3 (Assumes initialization state) &amp;C0 (Carrier detect forced) &amp;C1 (Carrier detect true) Y0 (Disable long space disconnect) Y1 (Enable log space disconnect) ped Options</pre>
This reg and Y con individu Bit 0 1 2 3,4 5 6 7 Register This reg	ister is a bit mmands. You do al commands to Value Not used. Not used. 00 10 01 11 0 1 Not used. 0 1 S22 - Bit Map ister is a bit	<pre>-mapped register that shows the status of the &amp;D,&amp;C, not normally write to this register. Use the affect these options. Description &amp;D0 (Forced DTR) &amp;D1 (Async command state) &amp;D2 (On hook async command state, disables auto answer) &amp;D3 (Assumes initialization state) &amp;C0 (Carrier detect forced) &amp;C1 (Carrier detect true) Y0 (Disable long space disconnect) Y1 (Enable log space disconnect) ped Options -mapped register that shows the status of the M,X, and</pre>
This reg and Y con individu Bit 0 1 2 3,4 5 6 7 Register This reg	ister is a bit mmands. You do al commands to Value Not used. Not used. 00 10 01 11 0 1 Not used. 0 1 S22 - Bit Map ister is a bit	<pre>-mapped register that shows the status of the &amp;D,&amp;C, not normally write to this register. Use the affect these options. Description &amp;D0 (Forced DTR) &amp;D1 (Async command state) &amp;D2 (On hook async command state, disables auto answer) &amp;D3 (Assumes initialization state) &amp;C0 (Carrier detect forced) &amp;C1 (Carrier detect true) Y0 (Disable long space disconnect) Y1 (Enable log space disconnect) ped Options</pre>
This reg and Y co individu Bit 0 1 2 3,4 5 6 7 Register This reg &P comma	ister is a bit mmands. You do al commands to Value Not used. Not used. 00 10 01 11 0 1 Not used. 0 1 S22 - Bit Map ister is a bit nds. You do no	<pre>-mapped register that shows the status of the &amp;D,&amp;C, not normally write to this register. Use the affect these options. Description &amp;D0 (Forced DTR) &amp;D1 (Async command state) &amp;D2 (On hook async command state, disables auto answer) &amp;D3 (Assumes initialization state) &amp;C0 (Carrier detect forced) &amp;C1 (Carrier detect true) Y0 (Disable long space disconnect) Y1 (Enable log space disconnect) ped Options -mapped register that shows the status of the M,X, and t normally write to this register. Use the individual</pre>
This reg and Y co individu Bit 0 1 2 3,4 5 6 7 Register This reg &P comma commands	ister is a bit mmands. You do al commands to Value Not used. Not used. Not used. 0 10 01 11 0 1 Not used. 0 1 S22 - Bit Map ister is a bit nds. You do no to affect the	<pre>-mapped register that shows the status of the &amp;D,&amp;C, not normally write to this register. Use the affect these options. Description &amp;D0 (Forced DTR) &amp;D1 (Async command state) &amp;D2 (On hook async command state, disables auto answer) &amp;D3 (Assumes initialization state) &amp;CO (Carrier detect forced) &amp;C1 (Carrier detect true) Y0 (Disable long space disconnect) Y1 (Enable log space disconnect) ped Options -mapped register that shows the status of the M,X, and t normally write to this register. Use the individual se options.</pre>
This reg and Y cou individu Bit 0 1 2 3,4 5 6 7 Register This reg &P comma commands Bit	ister is a bit mmands. You do al commands to Value Not used. Not used. 00 10 01 11 0 1 Not used. 0 1 S22 - Bit Map ister is a bit nds. You do no to affect the Value	<pre>-mapped register that shows the status of the &amp;D,&amp;C, not normally write to this register. Use the affect these options. Description &amp;D0 (Forced DTR) &amp;D1 (Async command state) &amp;D2 (On hook async command state, disables auto answer) &amp;D3 (Assumes initialization state) &amp;C0 (Carrier detect forced) &amp;C1 (Carrier detect true) Y0 (Disable long space disconnect) Y1 (Enable log space disconnect) ped Options -mapped register that shows the status of the M,X, and t normally write to this register. Use the individual</pre>
This reg and Y con individu Bit 0 1 2 3,4 5 6 7 Register This reg &P comma commands Bit 0,1	ister is a bit mmands. You do al commands to Value Not used. Not used. Not used. 0 1 Not used. 0 1 S22 - Bit Map ister is a bit nds. You do no to affect the Value Not used.	<pre>-mapped register that shows the status of the &amp;D,&amp;C, not normally write to this register. Use the affect these options. Description &amp;D0 (Forced DTR) &amp;D1 (Async command state) &amp;D2 (On hook async command state, disables auto answer) &amp;D3 (Assumes initialization state) &amp;CO (Carrier detect forced) &amp;C1 (Carrier detect true) Y0 (Disable long space disconnect) Y1 (Enable log space disconnect) Y1 (Enable log space disconnect) ped Options -mapped register that shows the status of the M,X, and t normally write to this register. Use the individual se options. Description</pre>
This reg and Y cou individu Bit 0 1 2 3,4 5 6 7 Register This reg &P comma commands Bit	ister is a bit mmands. You do al commands to Value Not used. Not used. 00 10 01 11 0 1 Not used. 0 1 S22 - Bit Map ister is a bit nds. You do no to affect the Value Not used. 00	<pre>-mapped register that shows the status of the &amp;D,&amp;C, not normally write to this register. Use the affect these options. Description &amp;D0 (Forced DTR) &amp;D1 (Async command state) &amp;D2 (On hook async command state, disables auto answer) &amp;D3 (Assumes initialization state) &amp;CO (Carrier detect forced) &amp;C1 (Carrier detect forced) &amp;C1 (Carrier detect true) Y0 (Disable long space disconnect) Y1 (Enable log space disconnect) Ped Options -mapped register that shows the status of the M,X, and t normally write to this register. Use the individual se options. Description M0 (Speaker disabled)</pre>
This reg and Y con individu Bit 0 1 2 3,4 5 6 7 Register This reg &P comma commands Bit 0,1	ister is a bit mmands. You do al commands to Value Not used. Not used. 00 10 01 11 0 1 Not used. 0 1 S22 - Bit Map ister is a bit nds. You do no to affect the Value Not used. 00	<pre>-mapped register that shows the status of the &amp;D,&amp;C, not normally write to this register. Use the affect these options. Description &amp;D0 (Forced DTR) &amp;D1 (Async command state) &amp;D2 (On hook async command state, disables auto answer) &amp;D3 (Assumes initialization state) &amp;CO (Carrier detect forced) &amp;C1 (Carrier detect true) Y0 (Disable long space disconnect) Y1 (Enable log space disconnect) Y1 (Enable log space disconnect) ped Options -mapped register that shows the status of the M,X, and t normally write to this register. Use the individual se options. Description</pre>

	01	M2(Speaker always on)
	11	M3(Speaker disabled during dialing & data transfer)
4,5,6	000	XO
	001	X1(Result code status)
	101	X2(Result code status)
	011	X3(Result code status)
	111	X4(Result code status)
7	0	&PO(U.S. make/break dial pulse ratio)
	1	&P1(U.K./Hong Kong make/break dial pulse ratio)

BTT	CHART
	CINUL

7 6 5	4	3	2	1	0
-------	---	---	---	---	---

Register S23 - Bit Mapped Options

This register is a bit-mapped register that shows your modem's current communications settings, and the status of the &G command. You do not normally write to this register. The modem sets the communications settings automatically.

Bit	Value	Description
0	0	Response to remote digital loopback disabled.
	1	Response to remote digital loopback enabled.
1,2	00	300 baud
	10	1200 baud
	11	2400 baud
3	Not used.	
4,5	00	Even parity
	10	Space parity
	01	Odd parity
	11	Mark/No parity
6,7	00	&GO(No guard tone)
	10	&G1(550 Hz guard tone)
	01	&G2(1800 Hz guard tone)

Bit Chart

7 6 5 4 3 2 1	5 4 3 2 1	0
---------------	-----------	---

Registers S24-S26 - Not Used

Registers S27 - Bit Mapped Options This register is a bit-mapped register that shows the status of the B command. You do not normally write to this register. Use the B command to affect the status of this register.

Bit	Value	Description
0-5	Not used.	
6	0	B0 CCITT V.22bis/V.22
	1	B1 Bell 212A
7	Not used.	

Bit Chart

7 6 5 4	3 2	1 0
---------	-----	-----

(smm 07/28/93)